

PRE-DRIVE CIRCUIT, CAPACITIVE LOAD DRIVE CIRCUIT AND  
PLASMA DISPLAY APPARATUS

5

ABSTRACT OF THE DISCLOSURE

10       A pre-drive circuit having low deviation of timing  
of a high level and a low level output voltages is  
disclosed. A plurality of drive systems are comprised,  
each having an input amplifier circuits for amplifying  
input voltages input to input voltage terminals, high  
level shift circuits for shifting signal levels output  
15       from the input amplifier circuits, and output amplifier  
circuits for amplifying shift signals output from the  
high level shift circuits, and each drive system has the  
same constitution.